

Term	Explanation
SMT (Satisfiability Modulo Theories) Solver	Checks validity or satisfiability of logical formulas automatically. Example solvers are Yices from SRI and Z3 from Microsoft.
Hypothesis Space	Defines the search space of candidate functions that the synthesizer has to search over. Can be infinite.
Oracle	A human or an automated solver which answers questions posed by the synthesizer.
Interaction model	The way the synthesizer and the oracle interact. Concretely, refers to particular kinds of questions asked by the synthesizer, and answers given by the oracle.
CEGIS (Counter-Example Guided Inductive Synthesis)	Refers to a particular interaction model between a synthesizer and an oracle, where the oracle provides counter-examples (so the oracle is more than just a classifier which says yes/no, but also gives a reason for the failure).
Domain Specific Language (DSL)	A programming language targeting particular application domain. Typically used to specify the hypothesis space
Initial Specification	The initial specification provided to the synthesizer. This may be a logical spec or just a set of examples.

	Covered in	Initial Specification	Hypothesis Space	Interaction Model	Supported Oracle Queries	Who is the Oracle?	Is SMT used?
Sketch	Lecture 2, Lecture 3, slide 17	Naïve implementation	Programs with “holes”	CEGIS	Equivalence – oracle returns ‘yes’, or counter-example	SAT/SMT solver	yes
Flash-Fill	Lecture 3, slides 7-13	A set of input-output examples	DSL of string manipulation functions	None	None	None	no
Oracle-guided	Lecture 3, slide 17	None	Programs which are permutation of a predefined set of components	Synthesizer poses membership queries to oracle	Membership – oracle returns yes/no	Human	Yes, to find: P, P', in
SPEX	Lecture 3, slides 18-44 Exercise 3	None/one input-output example	Disjunctive/Conjunctive Formulas	Synthesizer poses membership queries to oracle	Membership – oracle returns yes/no	Human	No

Sketch + CEGIS

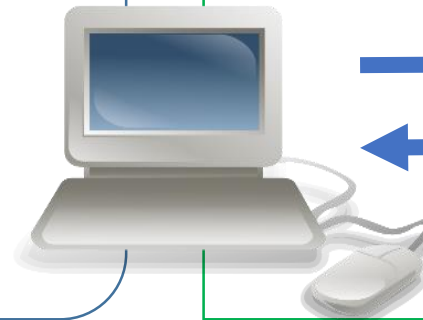
Oracle is often implemented by a constraint-solver, e.g., SMT

Sketch

```
bit[W] isolate0Sketched(bit[W] x) {  
    return ~(x + ??) & (x + ??);  
}
```

CEGIS

```
bit[W] isolate0Fast (bit[W] x)  
    return ~x & (x+1);
```



Yes/counterexample



Flash Fill

User provides a set of examples already, so it does not use an oracle to answer queries. It also does not use an SMT solver.

PBE Synthesizer

```
String expr  $P$  := Switch(( $b_1, e_1$ ), .., ( $b_n, e_n$ ))
Bool  $b$  :=  $d_1 \vee \dots \vee d_n$ 
Conjunct  $d$  :=  $\pi_1 \wedge \dots \wedge \pi_n$ 
Predicate  $\pi$  := Match( $v_i, r, k$ ) |  $\neg$  Match( $v_i, r, k$ )
Trace expr  $e$  := Concatenate( $f_1, \dots, f_n$ )
Atomic expr  $f$  := SubStr( $v_i, p_1, p_2$ )
                | ConstStr( $s$ )
                | Loop( $\lambda w : e$ )
Position  $p$  := CPos( $k$ ) | Pos( $r_1, r_2, c$ )
Integer expr  $c$  :=  $k$  |  $k_1 w + k_2$ 
Regular Expression  $r$  := TokenSeq( $T_1, \dots, T_m$ )
Token  $T$  :=  $C +$  |  $[-C] +$ 
           | SpecialToken
```

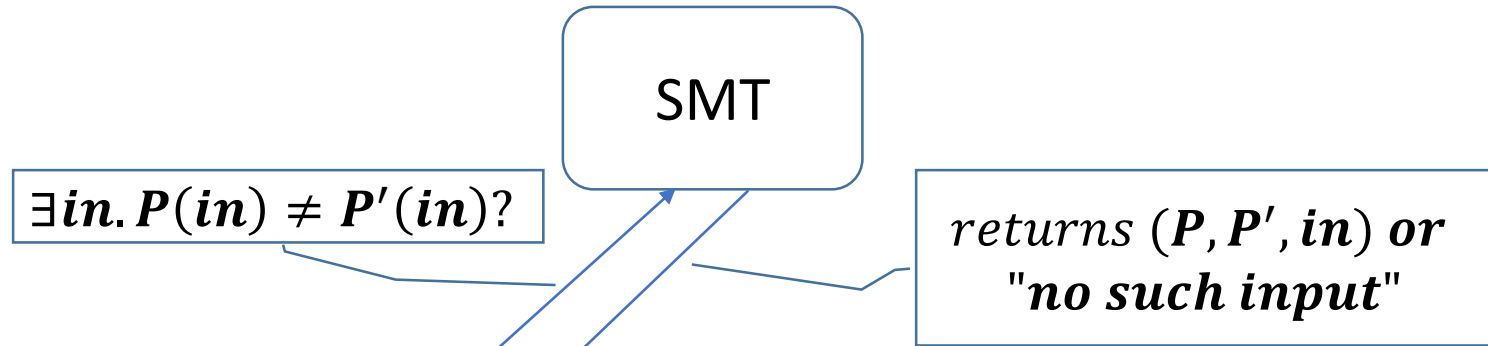
PBE

Input-output examples



function

Oracle-guided (Jha et al., 2010)



Oracle-guided

Component 1

Component 2

Component k

PBE

(Olivia, F, 15)

Send/don't send email

function



Exact PBE

Hypothesis space and interaction model are such that one can provide guarantees on the number of questions.

Exact PBE Synthesizer

A set of predicates Q +

```
String expr  $P$  := Switch(( $b_1, e_1$ ), .., ( $b_n, e_n$ ))
Bool  $b$  :=  $d_1 \vee \dots \vee d_n$ 
Conjunct  $d$  :=  $\pi_1 \wedge \dots \wedge \pi_n$ 
Predicate  $\pi$  := Match( $v_i, r, k$ ) |  $\neg$  Match( $v_i, r, k$ )
Trace expr  $e$  := Concatenate( $f_1, \dots, f_n$ )
Atomic expr  $f$  := SubStr( $v_i, p_1, p_2$ )
                | ConstStr( $s$ )
                | Loop( $\lambda w : e$ )
Position  $p$  := CPos( $k$ ) | Pos( $r_1, r_2, c$ )
Integer expr  $c$  :=  $k$  |  $k_1 w + k_2$ 
Regular Expression  $r$  := TokenSeq( $T_1, \dots, T_m$ )
Token  $T$  :=  $C +$  | [ $\neg C$ ] +
           | SpecialToken
```

Exact PBE

(Olivia, F, 15)

Send/don't send email

function

